



KISHKINDA UNIVERSITY

Advancing Knowledge Transforming Lives

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Minutes of Board of Studies Meeting

Date: 01.05.2024

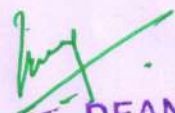
The Board of Studies Meeting of the Department of Electronics and Communication engineering was held online on 30.04.2024 at 02:30PM Via google meet platform to finalize and approve the scheme and syllabus for III and IV semesters. The meeting began with presentation of curriculum by chair for III and IV semesters. He then presented the syllabus of all subjects of III and IV semesters prepared by internal BOS members. The following members were present:

S.No	Name of the BOS Member	Designation	Signature
1	Dr.U.Eranna	Chairperson, Dean FET, Kishkinda University, Ballari	
2	Dr.Sadyojatha K.M	External BOS Member from other Institute	
3	Dr.V.CPatil	External BOS Member from other University	
4	Dr.PatilRamana Reddy	External BOS Member from other University	online
5	Dr.Seshachalam	External BOS Member from other University	online
6	Dr. Subhash Kulkarni	External BOS Member from other University	online
7	Mr.Reddy Sridhar	External BOS Member from Industry	online
8	Dr.M.Sreenivasulu	Internal Member, Associate Professor, Department of ECE, Kishkinda University	
9	Mrs.R.G.Hemalatha	Internal Member, Assistant Professor, Department of ECE, Kishkinda University	

The following suggestions were given by the Board of Studies Committee members during the meeting on the above said date.

CURRICULUM RELATED SUGGESTIONS:

- 1) The BOS Committee members has suggested to include Signal and Systems as a basic engineering subject in the III-Semester.
- 2) The BOS Committee members has proposed to include Fourier series and Fourier Transform topics in the signals and systems subject.


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- 3) The committee decided to use MATLAB or equivalent software to conduct the Signals and Systems laboratory. Also it is resolved to avoid built-in functions as much as possible while conducting laboratory.
- 4) The Vice Chancellor of Kishkinda University and External BOS member from other University Dr. Patil Ramana Reddy has suggested to include **Probability theory** in basic mathematics subject either in the III-Semester or IV-Semester.
- 5) The external BOS committee members has proposed to Include FET's and MOSFET's in the Analog Electronic Circuits(AEC) subject.
- 6) The BOS committee members has suggested to concentrate much on Chip Designing Courses like VLSI (VHDL and Verilog Programming) by making use of the software tools XILINX and Cadence.
- 7) The BOS committee members has suggested to limit the number of experiments and include two or three case studies in every core Laboratories.
- 8) The external BOS committee members has suggested to reframe the syllabus of Computer Organization and Architecture Subject by taking the help of Mr. Reddy Sridhar.


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